

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary



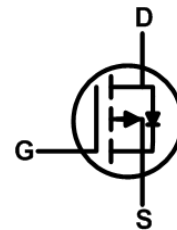
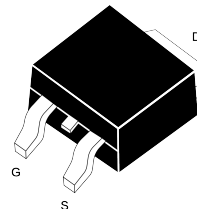
BVDSS	RDSON	ID
-40V	13.5mΩ	-40A

Description

The JH40P04 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The JH40P04 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

TO-252 Pin Configuration



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-40	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	-40	A
	$T_C = 100^\circ\text{C}$		-22	
Pulsed Drain Current ¹		I_{DM}	-140	A
Single Pulse Avalanche Energy ²		EAS	57.8	mJ
Total Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	40.3	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	$R_{\theta JA}$	66	$^\circ\text{C/W}$
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	3.1	$^\circ\text{C/W}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-40	-	-	V
Gate-body Leakage current		I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T _J =25°C	I _{DSS}	V _{DS} = -40V, V _{GS} = 0V	-	-	-1	μA
	T _J =100°C			-	-	-100	
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.0	-1.5	-2.2	V
Drain-Source On-Resistance ⁴		R _{DS(on)}	V _{GS} = -10V, I _D = -20A	-	13.5	19	mΩ
			V _{GS} = -4.5V, I _D = -15A	-	19.5	25	
Forward Transconductance ⁴		g _{fs}	V _{DS} = -10V, I _D = -20A	-	44	-	S
Dynamic Characteristics ⁵							
Input Capacitance		C _{iss}	V _{DS} = -20V, V _{GS} =0V, f =1MHz	-	2525	-	pF
Output Capacitance		C _{oss}		-	190	-	
Reverse Transfer Capacitance		C _{rss}		-	172	-	
Gate Resistance		R _g	f =1MHz	-	10	-	Ω
Switching Characteristics ⁵							
Total Gate Charge		Q _g	V _{GS} = -10V,V _{DS} = -20V, I _D = -20A	-	35	-	nC
Gate-Source Charge		Q _{gs}		-	5.5	-	
Gate-Drain Charge		Q _{gd}		-	8	-	
Turn-On Delay Time		t _{d(on)}	V _{GS} = -10V, V _{DD} = -20V, R _G = 3Ω, I _D = -20A	-	14.5	-	ns
Rise Time		t _r		-	20.2	-	
Turn-Off Delay Time		t _{d(off)}		-	32	-	
Fall Time		t _f		-	10	-	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage ⁴		V _{SD}	I _S = -20A, V _{GS} = 0V	-	-	-1.2	V
Continuous Source Current	T _C =25°C	I _S	-	-	-	-40	A

Note :

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ\text{C}$.
2. The EAS data shows Max. rating . The test condition is $V_{DD} = -25V, V_{GS} = -10V, L = 0.1\text{mH}, I_{AS} = -34A$.
3. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

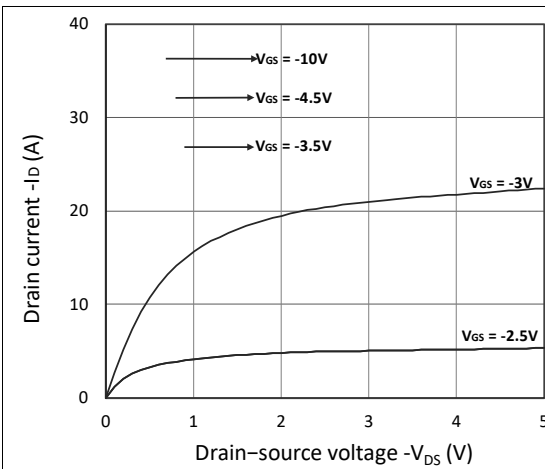


Figure 1. Output Characteristics

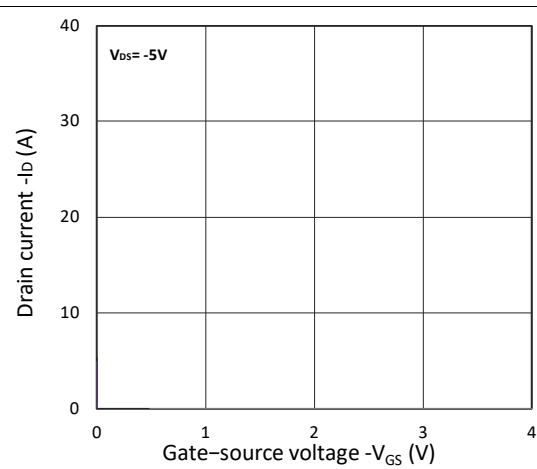


Figure 2. Transfer Characteristics

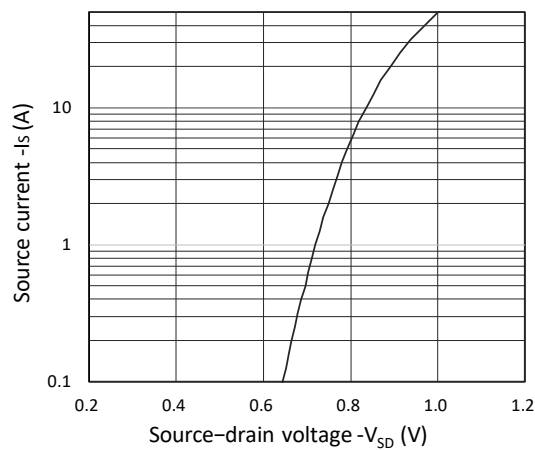


Figure 3. Forward Characteristics of Reverse

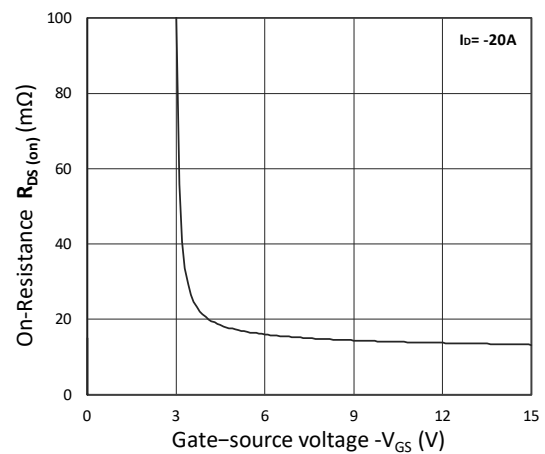


Figure 4. $R_{DS(on)}$ vs. V_{GS}

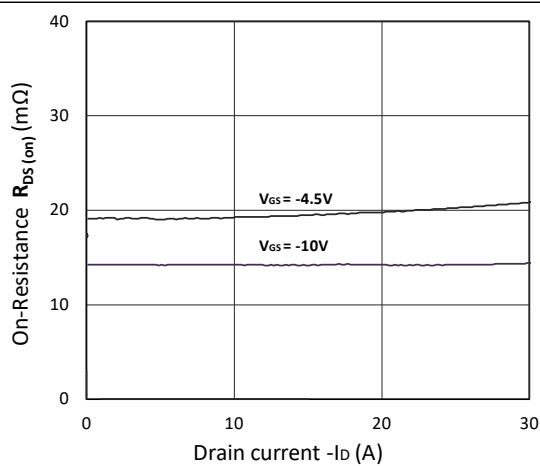


Figure 5. $R_{DS(on)}$ vs. I_D

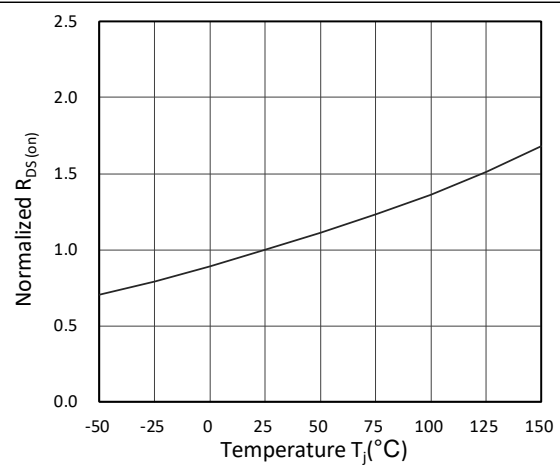


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

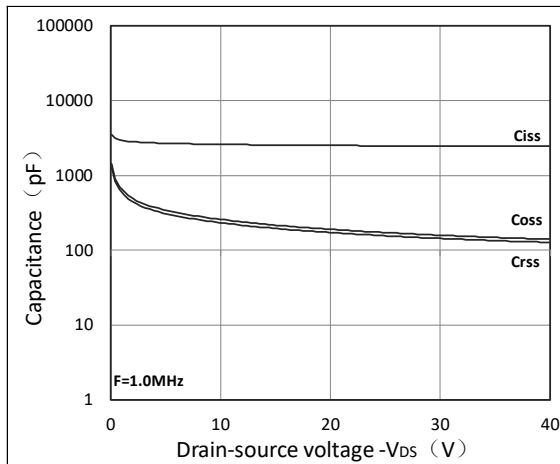


Figure 7. Capacitance Characteristics

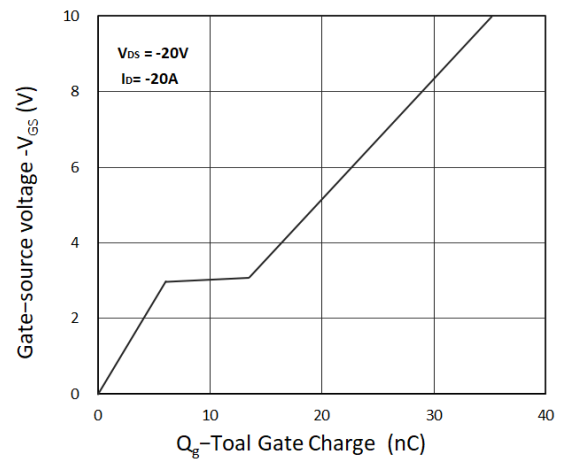


Figure 8. Gate Charge Characteristics

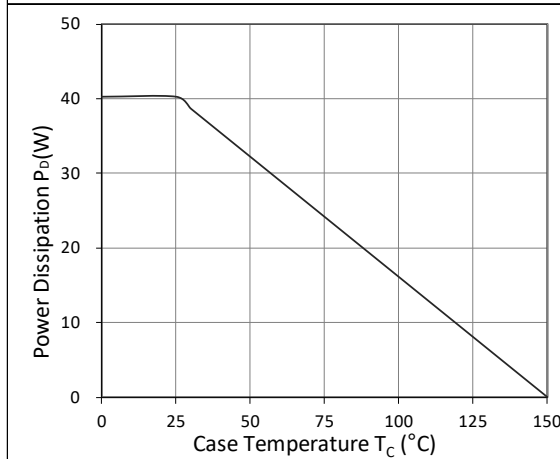


Figure 9. Power Dissipation

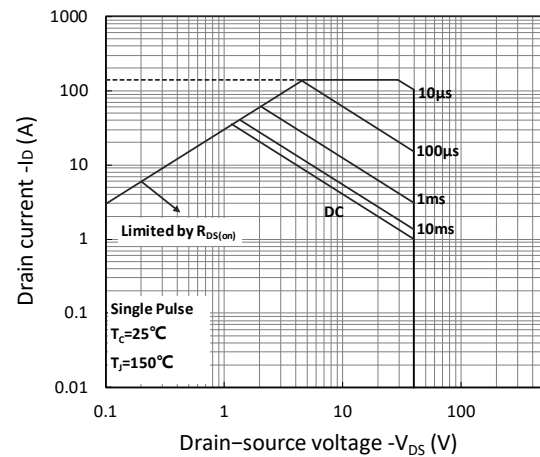


Figure10. Safe Operating Area

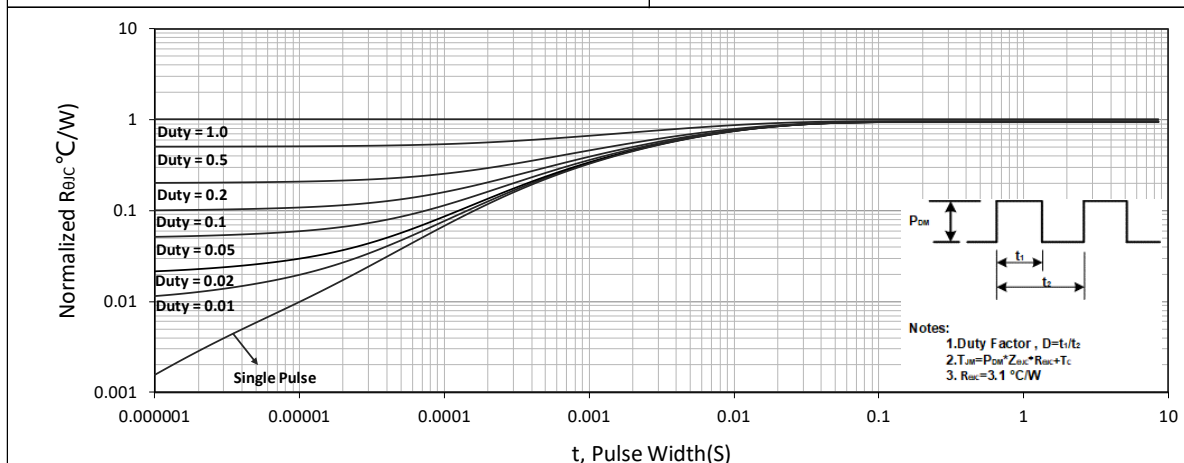


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

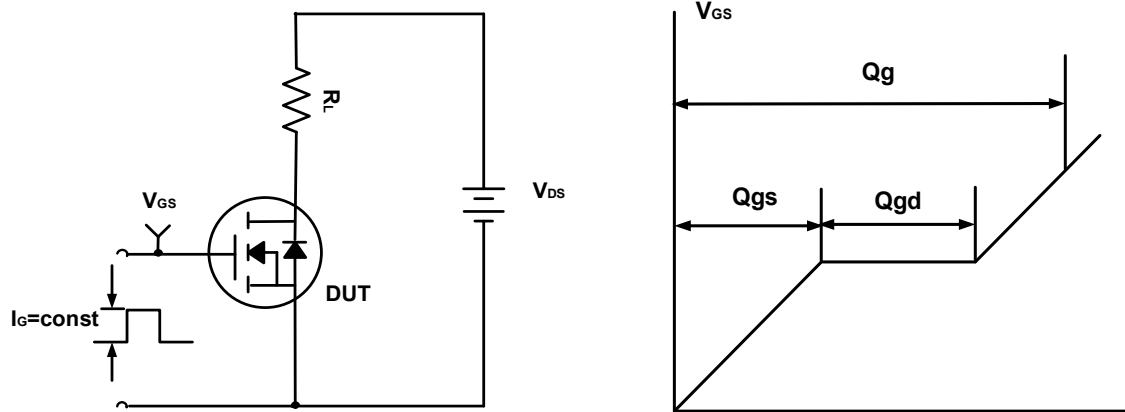


Figure A. Gate Charge Test Circuit & Waveforms

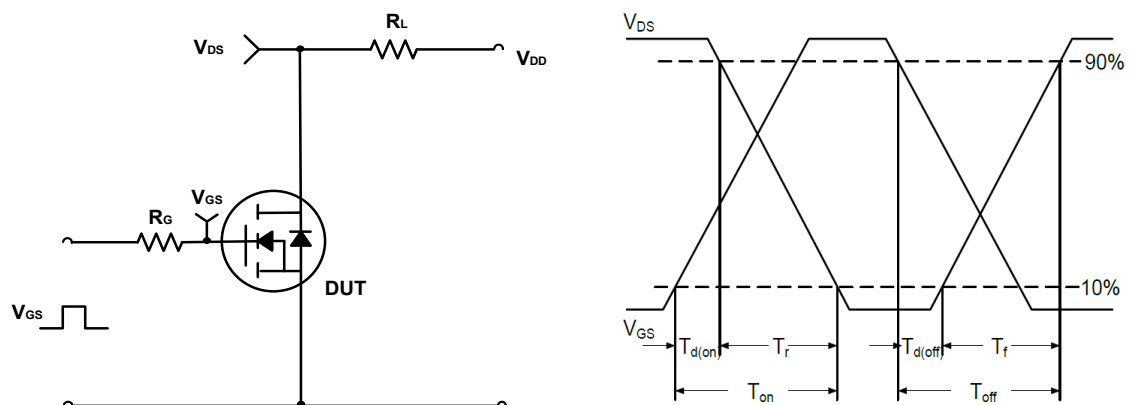


Figure B. Switching Test Circuit & Waveforms

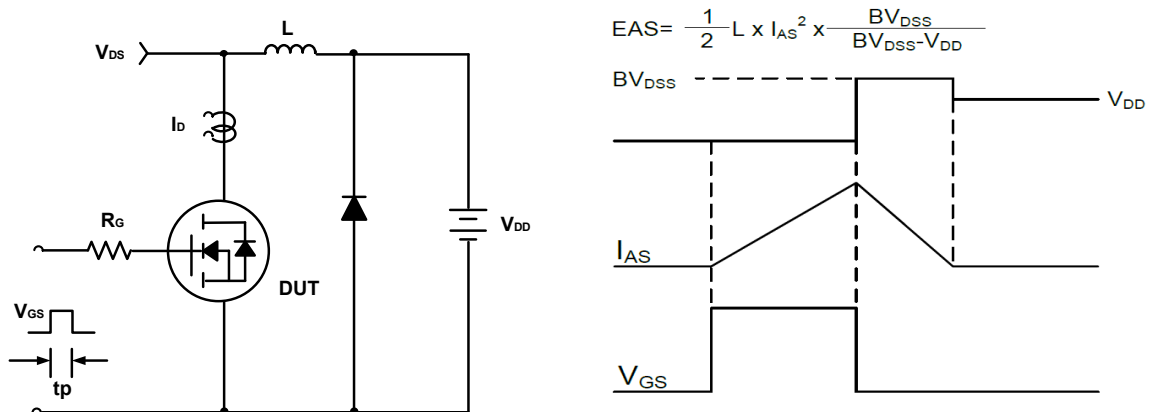
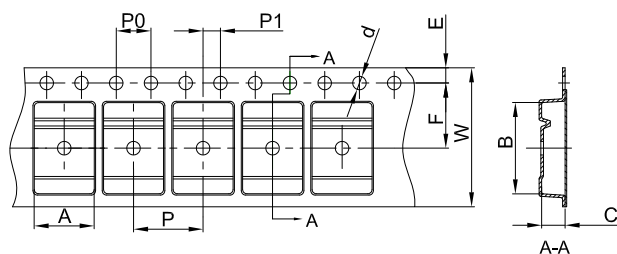


Figure C. Unclamped Inductive Switching Circuit & Waveforms

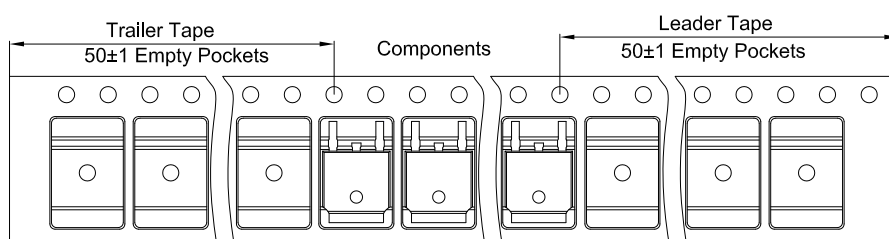
TO-252-2L Tape and Reel

TO-252 Embossed Carrier Tape

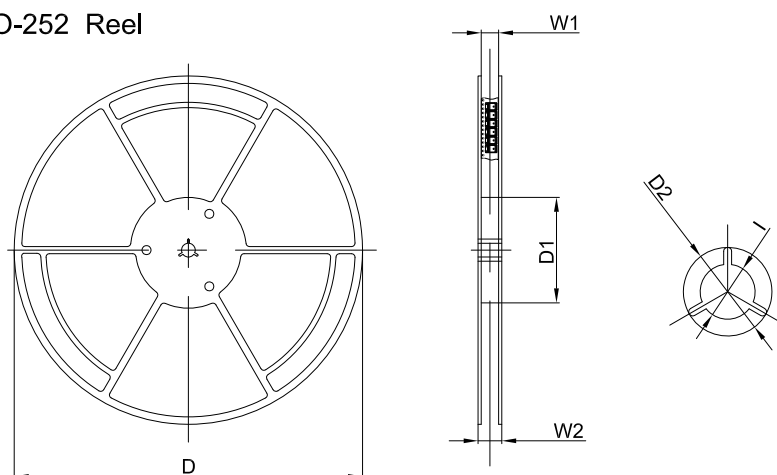


Dimensions are in millimeter										
Pkg type	A	B	C	d	E	F	P0	P	P1	W
TO-252	6.90	10.50	2.70	Ø1.55	1.75	7.50	4.00	8.00	2.00	16.00

TO-252 Tape Leader and Trailer

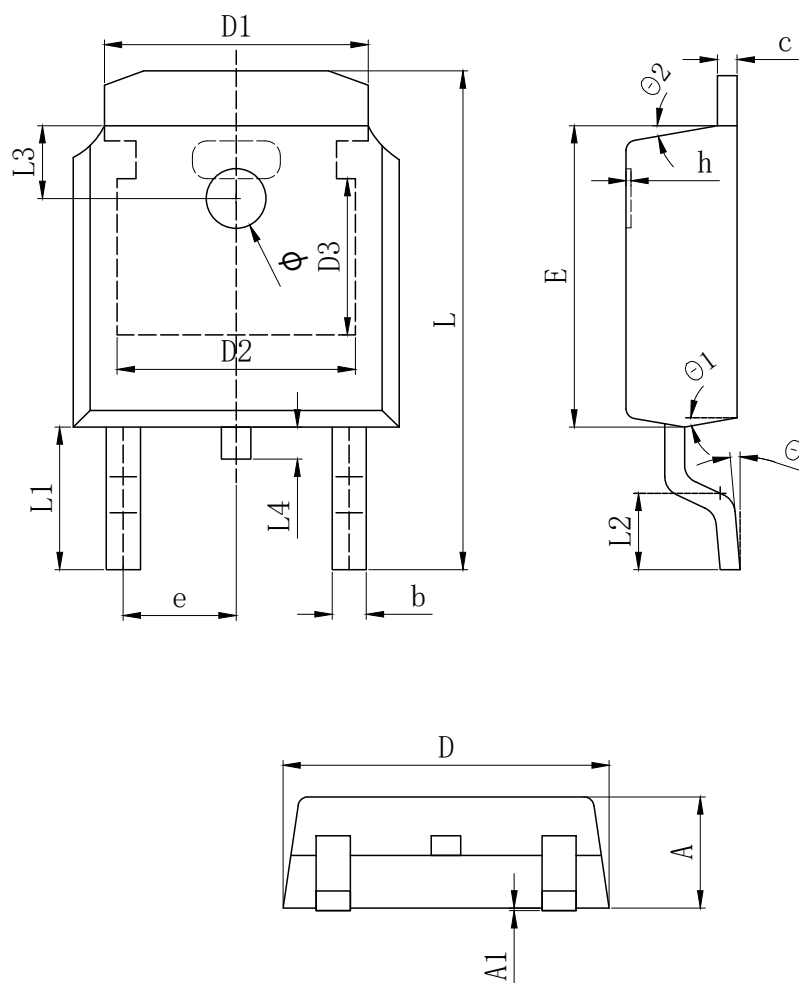


TO-252 Reel



Dimensions are in millimeter						
Reel Option	D	D1	D2	W1	W2	I
13"Dia	330.00	100.00	Ø21.00	16.40	21.00	Ø13.00

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
2,500 pcs	13inch	2,500 pcs	340×336×29	25,000 pcs	353×346×365	



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ_1	9° TYP		
θ_2	9° TYP		

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